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ABSTRACT OF THE DISCLOSURE

A multiplex conversion unit is configured with the functions required for a specific network to which it is applicable more economically. Each OC-12IF circuit pack includes a switch for performing time slot assignment (TSA) between a plurality of high-speed transmission lines accommodated in the OC-12IF circuit pack and at least a low-speed transmission line accommodated in at least a DS3IF circuit pack. circuit pack includes a switch for performing time slot interchange (TSI) between the high-speed transmission lines and the low-speed\transmission line. application to a network requiring the TSI function, the switch of the OC-12IF carcuit pack performs only the add/drop multiplex (ADM) δ peration between the signals input/output by the high-speed transmission lines and the ADM circuit pack, and the switch on the ADM circuit pack performs the TSI operation. application not requiring the TSI, on the other hand, the TSA operation is performed by the switch of the OC-12IF circuit pack using a THRU circuit pack having a wiring connecting the OC-12IF circuit pack and a DS3IF circuit pack in place of the ADM circuit pack